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Chen, M.-S.; Shin, K.G.;
Parallel and Distributed Systems, IEEE Transactions on
Volume 1, Issue 2, April 1990 Page(s):152 - 159
[AbstractPlus](#) | Full Text: [PDF\(748 KB\)](#) IEEE JNL
- ☐ 2. **Adaptive fault-tolerant routing in hypercube multicomputers**
Chen, M.-S.; Shin, K.G.;
Computers, IEEE Transactions on
Volume 39, Issue 12, Dec. 1990 Page(s):1406 - 1416
[AbstractPlus](#) | Full Text: [PDF\(952 KB\)](#) IEEE JNL
- ☐ 3. **Subcube allocation and task migration in hypercube multiprocessors**
Chen, M.-S.; Shin, K.G.;
Computers, IEEE Transactions on
Volume 39, Issue 9, Sept. 1990 Page(s):1146 - 1155
[AbstractPlus](#) | Full Text: [PDF\(852 KB\)](#) IEEE JNL
- ☐ 4. **Task Migration In Hypercube Multiprocessors**
Ming-Syan Chen; Kang G. Shin;
Computer Architecture, 1989. The 16th Annual International Symposium on
28 May - 1 June, 1989 Page(s):105 - 111
[AbstractPlus](#) | Full Text: [PDF\(532 KB\)](#) IEEE CNF

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Check to search only within this results set

Display Format: ☒ Citation ☐ Abstract

1. An engineering environment for hardware/software co-simulation

Becker, D.; Singh, R.K.; Tell, S.G.;

Design Automation Conference, 1992. Proceedings., 29th ACM/IEEE

8-12 June 1992 Page(s):129 - 134

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

7. Hardware/software selected cycle solution

Wilson, J.;

Hardware/Software Codesign, 1994. Proceedings of the Third International Workshop

22-24 Sept. 1994 Page(s):190 - 194

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF

16. Hardware/software co-design of a fingerprint recognition system
Sagar, V.K.; Greening, C.; Tai, W.Y.; Leung, C.S.A.;
Partitioning in Hardware-Software Codesigns, IEE Colloquium on
13 Feb 1995 Page(s):10/1 - 10/5
AbstractPlus | Full Text: EDE(216 KB) IEEE CNF
17. A co-design methodology based on formal specification and high-level estimation
Carreras, C.; Lopez, J.C.; Lopez, M.L.; Delgado-Kloos, C.; Martinez, N.; Sanchez, L.;
Hardware/Software Co-Design, 1996. (Codes/CASHE '96), Proceedings., Fourth Intern
on
18-20 March 1996 Page(s):28 - 35
AbstractPlus | Full Text: EDE(728 KB) IEEE CNF
18. Compiled HW/SW co-simulation
Zvojnovic, V.; Meyr, H.;
Design Automation Conference Proceedings 1996, 33rd
3-7 June 1996 Page(s):690 - 695
AbstractPlus | Full Text: EDE(560 KB) IEEE CNF
19. Miami: a hardware software co-simulation environment
Klein, R.;
Rapid System Prototyping, 1996. Proceedings., Seventh IEEE International Workshop
19-21 June 1996 Page(s):173 - 177
AbstractPlus | Full Text: EDE(524 KB) IEEE CNF
20. Symphony: a simulation backplane for parallel mixed-mode co-simulation of VLSI
Tedesco, A.R.W.; Meng, T.H.-Y.;
Design Automation Conference Proceedings 1996, 33rd
3-7 June 1996 Page(s):149 - 154
AbstractPlus | Full Text: EDE(624 KB) IEEE CNF
21. High speed video board as a case study for hardware-software co-design
Herrmann, D.; Maas, E.; Trawny, M.; Ernst, R.; Ruffer, P.; Seltz, M.; Hsenzah, S.;
Computer Design: VLSI in Computers and Processors, 1996. ICCD '96, Proceedings.,
International Conference on
7-9 Oct. 1996 Page(s):185 - 190
AbstractPlus | Full Text: EDE(712 KB) IEEE CNF
22. Uninterpreted co-simulation for performance evaluation of hw/sw systems
Calvez, J.P.; Heller, D.; Pasquier, O.;
Hardware/Software Co-Design, 1996. (Codes/CASHE '96), Proceedings., Fourth Intern
on
18-20 March 1996 Page(s):132 - 139
AbstractPlus | Full Text: EDE(792 KB) IEEE CNF
23. LEAH: an introduction to behavioral abstraction and co-simulation using Perl and
Gallies, B.; Dorman, K.; Mednick, E.;
VLSI HDL Conference, 1996. Proceedings., 1996 IEEE International
26-28 Feb. 1996 Page(s):81 - 88
AbstractPlus | Full Text: EDE(736 KB) IEEE CNF
24. Hardware/software co-design for DSP applications via the HMS framework
Sheliga, M.; Sha, E.H.-M.;
Acoustics, Speech, and Signal Processing, 1996. ICASSP-96. Conference Proceedings
International Conference on
Volume 2, 7-10 May 1996 Page(s):1248 - 1251 vol. 2
AbstractPlus | Full Text: EDE(320 KB) IEEE CNF

<http://ieeexplore.ieee.org/search/searchresult.jsp?queryl=hardware+software&scope=meta...> 6/7/2005

25. Exploiting reconfigurability through high level synthesis
Mogson, G.M.;
Hardware-Software Cosynthesis for Reconfigurable Systems (Digest No. 1996/036), IE
22 Feb. 1996 Page(s):5/1 - 5/4
AbstractPlus | Full Text: EDE(400 KB) IEEE CNF
26. AKKA: a tool-kit for cosynthesis and prototyping
Tammema, K.; O'Neil, M.; Jantsch, A.; Henanti, A.;
Hardware-Software Cosynthesis for Reconfigurable Systems (Digest No. 1996/036), IE
22 Feb. 1996 Page(s):8/1 - 8/8
AbstractPlus | Full Text: EDE(536 KB) IEEE CNF
27. LOPIOM [programmable logic with outrageously massive interconnection]
Mosanya, E.; Linder, C.; Penier, J.-Y.; Rampogna, F.; Sanchez, E.;
Hardware-Software Cosynthesis for Reconfigurable Systems (Digest No. 1996/036), IE
22 Feb. 1996 Page(s):7/1 - 7/5
AbstractPlus | Full Text: EDE(528 KB) IEEE CNF
28. Hardware/software co-design of digital telecommunication systems
Bolsens, I.; De Man, H.J.; Lin, B.; Van Rompaey, K.; Vercauteren, S.; Verkest, D.;
Proceedings of the IEEE
Volume 85, Issue 3, March 1997 Page(s):391 - 419
AbstractPlus | References | Full Text: EDE(452 KB) IEEE JNL
29. Choosing a digital simulator
Hillawi, J.;
Design Automation Conference 1997, Proceedings of the ASP-DAC '97, Asia and Sou
28-31 Jan. 1997 Page(s):371 - 376
AbstractPlus | Full Text: EDE(540 KB) IEEE CNF
30. Dynamic Communication Models in Embedded System Co-Simulation
Hines, K.; Borriello, G.;
Design Automation Conference, 1997. Proceedings of the 34th
June 9-13, 1997 Page(s):395 - 400
AbstractPlus | Full Text: EDE(712 KB) IEEE CNF
31. A hardware/software co-simulation environment for micro-processor design with
and OS interface
Ito, Y.; Nakamura, Y.;
Design Automation Conference 1997, Proceedings of the ASP-DAC '97, Asia and Sou
28-31 Jan. 1997 Page(s):377 - 382
AbstractPlus | Full Text: EDE(572 KB) IEEE CNF
32. Selective focus as a means of improving geographically distributed embedded a
simulation
Hines, K.; Borriello, G.;
Rapid System Prototyping, 1997. Shortening the Path from Specification to Prototype
8th IEEE International Workshop on
24-26 June 1997 Page(s):58 - 62
AbstractPlus | Full Text: EDE(556 KB) IEEE CNF
33. Interactive code design for real-time embedded control systems: task graph genera
SAVHDL models
Org, S.A.; Tienyryja, K.; Jozwiak, L.;
EUROMICRO '97, New Frontiers of Information Technology., Proceedings of the 23rd
Conference

<http://ieeexplore.ieee.org/search/searchresult.jsp?queryl=hardware+software&scope=meta...> 6/7/2005

- 14 Sept. 1997 Page(s):172 - 181
AbstractPlus | Full Text: PDF(812 KB) IEEE CNF
34. VHDL-based performance modeling: an application of the PMW tool suite to an IC classification system
Annon, J.; Hein, C.;
VHDL International Users' Forum, 1997. Proceedings
19-22 Oct. 1997 Page(s):209 - 215
AbstractPlus | Full Text: PDF(744 KB) IEEE CNF
35. VHDL models supporting a system-level design process: a RASSP approach
DeBardleben, J.A.; Madisetti, V.K.; Gadiant, A.J.;
VHDL International Users' Forum, 1997. Proceedings
19-22 Oct. 1997 Page(s):183 - 188
AbstractPlus | Full Text: PDF(632 KB) IEEE CNF
36. Trade-off evaluation in embedded system design via co-simulation
Passerone, C.; Lavagno, L.; Sansoe, C.; Chiodo, M.; Sangiovanni-Vincentelli, A.;
Design Automation Conference 1997. Proceedings of the ASP-DAC '97, Asia and South
28-31 Jan. 1997 Page(s):291 - 297
AbstractPlus | Full Text: PDF(820 KB) IEEE CNF
37. Modeling micro-controller peripherals for high-level co-simulation and synthesis
Haelh, H.; Lavagno, L.; Passerone, C.; Sansoe, C.; Sangiovanni-Vincentelli, A.;
Hardware/Software Codesign, 1997. (CODES/CASHE '97), Proceedings of the Fifth In
Workshop on
24-28 March 1997 Page(s):127 - 130
AbstractPlus | Full Text: PDF(336 KB) IEEE CNF
38. Hardware software partitioning using genetic algorithm
Saha, D.; Mitra, R.S.; Basu, A.;
VLSI Design, 1997. Proceedings., Tenth International Conference on
4-7 Jan. 1997 Page(s):155 - 160
AbstractPlus | Full Text: PDF(496 KB) IEEE CNF
39. On architectural styles for multimedia signal processors
Kung, S.Y.; Yen-Kuang Chen;
Multimedia Signal Processing, 1997., IEEE First Workshop on
23-25 June 1997 Page(s):427 - 432
AbstractPlus | Full Text: PDF(236 KB) IEEE CNF
40. A flexible model for evaluating the behavior of hardware/software systems
Allara, A.; Filippini, S.; Fomacant, W.; Salice, F.; Scudo, D.;
Hardware/Software Codesign, 1997. (CODES/CASHE '97), Proceedings of the Fifth In
Workshop on
24-28 March 1997 Page(s):109 - 114
AbstractPlus | Full Text: PDF(464 KB) IEEE CNF
41. Hardware-software co-design: Tools for architecting systems-on-a-chip
Gupta, R.K.;
Design Automation Conference 1997. Proceedings of the ASP-DAC '97, Asia and South
28-31 Jan. 1997 Page(s):285 - 289
AbstractPlus | Full Text: PDF(564 KB) IEEE CNF
42. Improving design turnaround time via two-levels HW/SW co-simulation
Allara, A.; Filippini, S.; Fomacant, W.; Salice, F.; Scudo, D.;
Computer Design: VLSI in Computers and Processors, 1997. ICCD '97, Proceedings.,
AbstractPlus | Full Text: PDF(748 KB) IEEE CNF

[http://ieeexplore.ieee.org/search/searchresult.jsp?query\[\]=hardware+software&scope!=meta...](http://ieeexplore.ieee.org/search/searchresult.jsp?query[]=hardware+software&scope!=meta...) 6/7/2005

- International Conference on
12-15 Oct. 1997 Page(s):400 - 405
AbstractPlus | Full Text: PDF(776 KB) IEEE CNF
43. MOGAC: a multiobjective genetic algorithm for the co-synthesis of hardware-software systems
Dick, R.P.; Jha, N.K.;
Computer-Aided Design, 1997. Digest of Technical Papers., 1997 IEEE/ACM International
on
9-13 Nov. 1997 Page(s):522 - 529
AbstractPlus | Full Text: PDF(928 KB) IEEE CNF
44. A co-design methodology for telecommunication systems: a case study of an access
canceller
Baganne, A.; Philippe, J.L.; Martin, E.;
Signal Processing Systems, 1997. SIPS 97 - Design and Implementation., 1997 IEEE
3-5 Nov. 1997 Page(s):273 - 282
AbstractPlus | Full Text: PDF(512 KB) IEEE CNF
45. The need for co-simulation in ASIC-verification
Spholm, S.; Lindh, L.;
EUROMICRO 97. New Frontiers of Information Technology., Proceedings of the 23rd
Conference
1-4 Sept. 1997 Page(s):331 - 335
AbstractPlus | Full Text: PDF(352 KB) IEEE CNF
46. Proceedings Tenth International Conference on VLSI Design
VLSI Design, 1997. Proceedings., Tenth International Conference on
4-7 Jan. 1997
AbstractPlus | Full Text: PDF(796 KB) IEEE CNF
47. Generation of the HDL-A-model of a micromembrane from its finite-element-dec
Hofmann, K.; Glesner, M.; Sebe, N.; Manolescu, A.; Marco, S.; Samtler, J.; Karam, J.;
European Design and Test Conference, 1997. ED&TC 97. Proceedings
17-20 March 1997 Page(s):108 - 112
AbstractPlus | Full Text: PDF(448 KB) IEEE CNF
48. System level memory optimization for hardware-software co-design
Dancand, K.; Calthor, F.; De Man, H.;
Hardware/Software Codesign, 1997. (CODES/CASHE '97), Proceedings of the Fifth In
Workshop on
24-28 March 1997 Page(s):55 - 59
AbstractPlus | Full Text: PDF(488 KB) IEEE CNF
49. VHDL-AleC++ co-simulation
Dimic, Z.; Damjanovic, M.; Lilevski, V.;
Microelectronics, 1997. Proceedings., 1997 21st International Conference on
Volume 2, 14-17 Sept. 1997 Page(s):725 - 728 vol.2
AbstractPlus | Full Text: PDF(356 KB) IEEE CNF
50. Efficient circuit partitioning to extend cycle simulation beyond synchronous circ
DeVane, C.J.;
Computer-Aided Design, 1997. Digest of Technical Papers., 1997 IEEE/ACM International
on
9-13 Nov. 1997 Page(s):154 - 161
AbstractPlus | Full Text: PDF(748 KB) IEEE CNF

[http://ieeexplore.ieee.org/search/searchresult.jsp?query\[\]=hardware+software&scope!=meta...](http://ieeexplore.ieee.org/search/searchresult.jsp?query[]=hardware+software&scope!=meta...) 6/7/2005

51. Fast Hardware/software Co-simulation For Virtual Prototyping And Trade-off Analysis
Passerone, C.; Lavagno, L.; Chiodo, M.; Sangiovanni-Vincentelli, A.;
Design Automation Conference, 1997. Proceedings of the 34th
June 9-13, 1997 Page(s):389 - 394
AbstractPlus | Full Text: PDF(636 KB) IEEE CNF
52. Optimizing communication in embedded system co-simulation
Hines, K.; Bortello, G.;
Hardware/Software Codesign, 1997. (CODES/CASHE '97). Proceedings of the Fifth in
Workshop on
24-26 March 1997 Page(s):121 - 125
AbstractPlus | Full Text: PDF(488 KB) IEEE CNF
53. Java as a specification language for hardware-software systems
Heidrich, R.; Olukotun, K.;
Computer-Aided Design, 1997. Digest of Technical Papers., 1997 IEEE/ACM International
on
9-13 Nov. 1997 Page(s):690 - 697
AbstractPlus | Full Text: PDF(736 KB) IEEE CNF
54. Hardware-software co-simulation of a parallel computer system
Shome, T.; McLeod, R.D.;
WESCANEX '97: Communications, Power and Computing, Conference Proceedings., I
22-23 May 1997 Page(s):214 - 217
AbstractPlus | Full Text: PDF(244 KB) IEEE CNF
55. Hardware/software Co-simulation In A Vhdl-based Test Bench Approach
Bauer, M.; Eckler, W.;
Design Automation Conference, 1997. Proceedings of the 34th
June 9-13, 1997 Page(s):774 - 779
AbstractPlus | Full Text: PDF(600 KB) IEEE CNF
56. A proposed design environment for dependable embedded systems
Bass, J.M.;
Hardware Systems for Dependable Applications (Digest No. 1997/335), IEEE Half-Day C
26 Nov. 1997 Page(s):4/1 - 4/3
AbstractPlus | Full Text: PDF(356 KB) IEEE CNF
57. Performance modeling for system design: an MPEG AV decoder example
Hocavar, D.E.; Shtam, S.; Ching-Yu Hung;
Circuits and Systems, 1998. ISCAS '98. Proceedings of the 1998 IEEE International S;
Volume 6, 31 May-3 June 1998 Page(s):203 - 206 vol.6
AbstractPlus | Full Text: PDF(392 KB) IEEE CNF
58. An energy conscious methodology for early design exploration of heterogeneous
Wan, M.; Ichikawa, Y.; Lidsky, D.; Rabaey, J.;
Custom Integrated Circuits Conference, 1998., Proceedings of the IEEE 1998
11-14 May 1998 Page(s):111 - 117
AbstractPlus | Full Text: PDF(720 KB) IEEE CNF
59. Mission success: meeting the systems acquisition challenge with electronic sys
automation
Peterson, G.D.; Mills, M.;
Aerospace and Electronics Conference, 1998. NAECON 1998. Proceedings of the IEEE
13-17 July 1998 Page(s):613 - 619
AbstractPlus | Full Text: PDF(772 KB) IEEE CNF

60. A data-flow oriented co-design for reconfigurable systems
David, J.-P.; Legat, J.-D.;
Rapid System Prototyping, 1998. Proceedings, 1998 Ninth International Workshop on
3-5 June 1998 Page(s):207 - 211
AbstractPlus | Full Text: PDF(244 KB) IEEE CNF
61. System-level modeling of DSP and embedded processors
Zvejnovic, V.; Schlager, C.; Fitzner, J.;
Signals, Systems & Computers, 1998. Conference Record of the Thirty-Second Asilom
Volume 2, 1-4 Nov. 1998 Page(s):1730 - 1734 vol.2
AbstractPlus | Full Text: PDF(536 KB) IEEE CNF
62. Communication synthesis approach for distributed systems and its application to
design of a communication controller
Zitouni, A.; Abid, M.; Tokri, K.; Souani, C.; Tourki, R.;
Microelectronics, 1998. ICMI '98. Proceedings of the Tenth International Conference on
14-16 Dec. 1998 Page(s):249 - 252
AbstractPlus | Full Text: PDF(468 KB) IEEE CNF
63. An image processing system developed under the codesign approach of COMIT
Adida, C.; Granger, X.; Lamety, P.; Boubal, M.;
Signals, Systems, and Electronics, 1998. ISSSE 98, 1998 URSI International Symposi
28 Sept.-2 Oct. 1998 Page(s):488 - 491
AbstractPlus | Full Text: PDF(332 KB) IEEE CNF
64. Intellectual property reuse in embedded system co-design: an industrial case st
Filippi, E.; Lavagno, L.; Luciani, L.; Montanaro, A.; Paolini, M.; Passerone, R.; Sgrol, I
Vinciguerra, A.;
System Synthesis, 1998. Proceedings, 11th International Symposium on
2-4 Dec. 1998 Page(s):37 - 42
AbstractPlus | Full Text: PDF(76 KB) IEEE CNF
65. The virtual chip set: a parametric IP library for system-on-a-chip design
Filippi, E.; Luciani, L.; Montanaro, A.; Paolini, M.; Turillo, M.; Tallarico, M.;
Custom Integrated Circuits Conference, 1998., Proceedings of the IEEE 1998
11-14 May 1998 Page(s):97 - 100
AbstractPlus | Full Text: PDF(484 KB) IEEE CNF
66. An approach to the design of a deterministic jitter-free dual-chamber brady pace
hardware and software cooperation
Marcotte, J.; Droppa, F.; Huang, J.C.; Johnson, S.;
Computers in Cardiology 1998
13-16 Sept. 1998 Page(s):549 - 552
AbstractPlus | Full Text: PDF(328 KB) IEEE CNF
67. Hw/Sw codesign of an ATM network interface card starting from a system level s
Zegainoh, N.E.; Marchioro, G.F.; Jerraya, A.A.;
Signals, Systems, and Electronics, 1998. ISSSE 98, 1998 URSI International Symposi
28 Sept.-2 Oct. 1998 Page(s):315 - 320
AbstractPlus | Full Text: PDF(708 KB) IEEE CNF
68. Arbitration schemes synthesis approach for multiprocessor systems
Zitouni, A.; Abid, M.; Tourki, R.;
Signal Processing Systems, 1998. SIPS 98, 1998 IEEE Workshop on
8-10 Oct. 1998 Page(s):499 - 508
AbstractPlus | Full Text: PDF(714 KB) IEEE CNF

69. Hardware/software co-design language compatible with VHDL
Wong, S.Y.;
WESCON98
15-17 Sept. 1998 Page(s):78 - 83
AbstractPlus | Full Text: PDF(368 KB) IEEE CNF
70. Buffer size driven partitioning for HW/SW co-design
Te-Cheng Lin; Sait, S.M.; Cyr, W.R.;
Computer Design: VLSI in Computers and Processors, 1998. ICCD '98. Proceedings.,
Conference on
5-7 Oct. 1998 Page(s):596 - 601
AbstractPlus | Full Text: PDF(48 KB) IEEE CNF
71. An arbiter synthesis approach based on arbitration scheme generation/selection
design
Zitouni, A.; Abid, M.; Tourki, R.;
Electronics, Circuits and Systems, 1998 IEEE International Conference on
Volume 1, 7-10 Sept. 1998 Page(s):521 - 526 vol.1
AbstractPlus | Full Text: PDF(476 KB) IEEE CNF
72. A high performant 0.5 μ m CMOS single chip ISDN network termination based on
core and co-processor architecture
Wouters, P.; Van Oostende, P.; Dawance, L.; Graindourze, B.;
Electronics, Circuits and Systems, 1998 IEEE International Conference on
Volume 2, 7-10 Sept. 1998 Page(s):533 - 536 vol.2
AbstractPlus | Full Text: PDF(400 KB) IEEE CNF
73. An efficient hardware/software co-design implementation for broadband telecom
applications
Rower, T.; Stadler, M.; Felber, N.; Fichtner, W.; Thalmann, M.;
Global Telecommunications Conference, 1998. GLOBECOM 98. The Bridge to Global
Volume 3, 8-12 Nov. 1998 Page(s):1676 - 1681 vol.3
AbstractPlus | Full Text: PDF(432 KB) IEEE CNF
74. A hardware/software co-design system using configurable computing technolog
Schweil, J.;
Parallel Processing Symposium, 1998. 1998 IPPS/SPP: Proceedings of the First Mar
International...and Symposium on Parallel and Distributed Processing 1998
30 March-3 April 1998 Page(s):620 - 625
AbstractPlus | Full Text: PDF(1100 KB) IEEE CNF
75. Proceedings. Ninth International Workshop on Rapid System Prototyping (Cat. N
Rapid System Prototyping, 1998. Proceedings, 1998 Ninth International Workshop on
3-5 June 1998
AbstractPlus | Full Text: PDF(1732 KB) IEEE CNF
76. Proposals toward an integrated design environment for complex embedded syst
Bass, J.M.;
Parallel and Distributed Processing, 1998. PDP '98. Proceedings of the Sixth Euro
21-23 Jan. 1998 Page(s):273 - 278
AbstractPlus | Full Text: PDF(624 KB) IEEE CNF
77. Proceedings of the Sixth International Workshop on Hardware/Software Codesig
(CODES/CASHE'98)
Hardware/Software Codesign, 1998. (CODES/CASHE '98) Proceedings of the Sixth in
Workshop on
15-18 March 1998

<http://ieeexplore.ieee.org/search/searchresult.jsp?query1=hardware+software&scope1=meta...> 6/7/2005


78. Language-based electronics systems design automation
Peterson, G.D.;
VLSI '98. System Level Design. Proceedings, IEEE Computer Society Workshop on
16-17 April 1998 Page(s):130 - 135
AbstractPlus | Full Text: PDF(60 KB) IEEE CNF
79. System hardware/software co-design ensures first pass success
Walters, G.;
WESCON98
15-17 Sept. 1998 Page(s):29 - 35
AbstractPlus | Full Text: PDF(736 KB) IEEE CNF
80. Advanced software radio architecture for 3rd generation mobile systems
Erben, H.; Sabatakakis, K.;
Vehicular Technology Conference, 1998. VTC 98. 48th IEEE
Volume 2, 18-21 May 1998 Page(s):825 - 829 vol.2
AbstractPlus | Full Text: PDF(564 KB) IEEE CNF
81. Integrated partitioning and scheduling for hardware/software co-design
Huqun Liu; Wong, D.F.;
Computer Design: VLSI in Computers and Processors, 1998. ICCD '98. Proceedings.,
Conference on
5-7 Oct. 1998 Page(s):609 - 614
AbstractPlus | Full Text: PDF(220 KB) IEEE CNF
82. Applying hardware/software co-design to systems-on-a-chip
Berger, A.S.;
WESCON98
15-17 Sept. 1998 Page(s):22 - 28
AbstractPlus | Full Text: PDF(608 KB) IEEE CNF
83. An interactive tool suite for real-time application development on heterogeneous
performance computing platforms
Patel, M.I.;
Digital Avionics Systems Conference, 1998. Proceedings., 17th DASC. The AIAA/IEEE
Volume 1, 31 Oct.-7 Nov. 1998 Page(s):823 - 1-9 vol.1
AbstractPlus | Full Text: PDF(1120 KB) IEEE CNF
84. Dynamic configuration: a new paradigm applicated to real time image analysis
Bourguiba, R.; Damigny, D.; Kessal, L.;
Microelectronics, 1998. (CM '98. Proceedings of the Tenth International Conference on
14-16 Dec. 1998 Page(s):25 - 28
AbstractPlus | Full Text: PDF(380 KB) IEEE CNF
85. Hardware, software and mechanical cosimulation for automotive applications
Le Marrec, P.; Valderama, C.A.; Hessel, F.; Jerraya, A.A.; Attia, M.; Cayrol, O.;
Rapid System Prototyping, 1998. Proceedings, 1998 Ninth International Workshop on
3-5 June 1998 Page(s):202 - 206
AbstractPlus | Full Text: PDF(312 KB) IEEE CNF
86. Verification of embedded real-time systems using hardware/software co-simulat
El Shobaki, M.;
EuroMicro Conference, 1998. Proceedings. 24th
Volume 1, 25-27 Aug. 1998 Page(s):46 - 50 vol.1
AbstractPlus | Full Text: PDF(392 KB) IEEE CNF

<http://ieeexplore.ieee.org/search/searchresult.jsp?query1=hardware+software&scope1=meta...> 6/7/2005

87. A top-down hardware/software co-simulation method for embedded systems bus component logical bus architecture
Yasuda, M.; Seo, K.; Kobayashi, H.; Shackelford, B.; Suzuki, F.; Design Automation Conference 1998. Proceedings of the ASP-DAC '98. Asia and South 10-13 Feb. 1998 Page(s):169 - 175
AbstractPlus | Full Text: PDF(562 KB) IEEE CNF
88. Performance evaluation tool for rapid prototyping of hardware-software codesign
Chatha, K.S.; Vemuri, R.; Rapid System Prototyping, 1998. Proceedings. 1998 Ninth International Workshop on 3-5 June 1998 Page(s):218 - 224
AbstractPlus | Full Text: PDF(208 KB) IEEE CNF
89. A geographically distributed framework for embedded system design and validation
Hines, K.; Bortello, G.; Design Automation Conference, 1998. Proceedings 15-19 Jun 1998 Page(s):140 - 145
AbstractPlus | Full Text: PDF(676 KB) IEEE CNF
90. A rapid prototyping co-simulation environment for neural control
Thomas, S.M.; Simulation '98. International Conference on (Conf. Publ. No. 457) 30 Sept.-2 Oct. 1998 Page(s):263 - 270
AbstractPlus | Full Text: PDF(620 KB) IEEE CNF
91. An open environment for the specification, design and code generation of control
Bass, J.M.; Open Control in the Process and Manufacturing Industries (Digest No. 1998/281), IEEE 15 May 1998 Page(s):771 - 774
AbstractPlus | Full Text: PDF(292 KB) IEEE CNF
92. New trends in intelligent system design for embedded and measurement applications
Alippi, C.; Ferrari, S.; Puri, V.; Sami, M.; Scotti, F.; Instrumentation & Measurement Magazine, IEEE Volume 2, Issue 2, June 1999 Page(s):36 - 44
AbstractPlus | Full Text: PDF(688 KB) IEEE JNL
93. The revolution in systems engineering
Hellestrand, G.R.; Spectrum, IEEE Volume 26, Issue 9, Sep 1999 Page(s):43 - 51
AbstractPlus | Full Text: PDF(676 KB) IEEE JNL
94. Behavioral modeling for hierarchical simulation of optoelectronic systems
Gaffiot, F.; Vuorinen, K.H.K.; Meyeuville, F.; O'Connor, I.; Jacquemod, G.; Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on (Circuits and Systems II: Express Briefs, IEEE Transactions on) Volume 46, Issue 10, Oct. 1999 Page(s):1316 - 1322
AbstractPlus | References | Full Text: PDF(208 KB) IEEE JNL
95. A software/hardware co-design methodology for embedded microprocessor control
Yong Zhang, Kai Kuang Ma, Qindong Yao; Consumer Electronics, IEEE Transactions on Volume 45, Issue 4, Nov 1999 Page(s):1241 - 1246
AbstractPlus | Full Text: PDF(248 KB) IEEE JNL

96. Simulation concert party
Clarke, P.; IEEE Review Volume 45, Issue 2, 18 March 1999 Page(s):82 - 84
AbstractPlus | Full Text: PDF(468 KB) IEEE JNL
97. Compiler/hardware co-design for instruction boosting in ILP processors
Wang, L.; Yang, T.C.; Computers and Digital Techniques, IEEE Proceedings-Volume 146, Issue 6, Nov. 1999 Page(s):269 - 274
AbstractPlus | Full Text: PDF(444 KB) IEEE JNL
98. Hardware/software co-design for IP objects based on CORBA
Coors, H.; Madrid, N.M.; Seepold, R.; Fall VILUF Workshop, 1999. 4-5 Oct. 1999 Page(s):63 - 68
AbstractPlus | Full Text: PDF(68 KB) IEEE CNF
99. An approach for interface generation in the PISH co-design system
Araujo, C.C.; Barros, E.; Integrated Circuits and Systems Design, 1999. Proceedings. XII Symposium on 29 Sept.-2 Oct. 1999 Page(s):66 - 69
AbstractPlus | Full Text: PDF(60 KB) IEEE CNF
100. Specification components: reusability at the HW/SW system specification level
Herrera, F.; Sanz, C.; Ugart, I.; Villar, E.; Fall VILUF Workshop, 1999. 4-5 Oct. 1999 Page(s):50 - 56
AbstractPlus | Full Text: PDF(1320 KB) IEEE CNF

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